

Electronic Acknowledgement Receipt

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Application Number:	10518779
International Application Number:	
Confirmation Number:	6609
Title of Invention:	Insulated-gate semiconductor device and approach involving junction-induced intermediate region
First Named Inventor/Applicant Name:	Kailash Gopalakrishnan
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		STFD035US_RCE.pdf	853552 <small>(231b02681708a544b61d8c14473c28128:1996)</small>	yes	16
Multipart Description/PDF files in .zip description					
	Document Description		Start		End
	Request for Continued Examination (RCE)		1		2
	Claims		3		12
	Applicant Arguments/Remarks Made in an Amendment		13		16
Warnings:					
Information:					
2	Fee Worksheet (PTO-875)	fee-info.pdf	30587 <small>(6aca702ce0f60cb084188f98152c80afa31cf:e0d)</small>	no	2
Warnings:					
Information:					
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National Stage of an International Application under 35 U.S.C. 371

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